THE ARRAY IC PACKAGING MARKET

2013 EDITION

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Chapter 1

INTRODUCTION

1.1 Background

The high demand for today's handheld electronics is requiring package solutions which can accommodate more I/O and more functionality than ever before.

Array packages fit that bill by allowing for more than just one row of I/O around the underside of the package as it attaches to the printed circuit board (PCB). Array packages are covered in this report which is described within.

1.2 Scope

This report covers these basic topics:

- BGA/LGA/CGA and FBGA/FLGA Packages
- QFN and Fan-In QFN Solutions
- WLP and Fan-Out WLP Solutions

1.3 Organization

This report is divided into six chapters and two appendices. They are as follows:

Chapter 1, Introduction: This chapter outlines the background, scope, organization, and methodology of the report.

Chapter 2, Executive Summary: This chapter provides summary forecasts and information.

Chapter 3, Economic Outlook and Worldwide Electronics Industry Forecast, is an overview of the state of the overall economy and semiconductor industry.

Chapter 4, BGA/LGA/CGA and FBGA/FLGA Package Solutions, breaks down the BGA and FBGA package families by their interconnections to the PCB by virtue of balls, land pads, or columns.

Chapter 5, Quad Flat Pack and Fan-In QFN Packages, covers the QFN package solution with emphasis on the Fan-in QFN package, which expands the I/O range which can be obtained with this package by adding additional rows of leads towards the interior of the package for an array pattern. Two or three rows are common at this time, turning this leadframe package into an array package solution.

Chapter 6, Wafer-Level Packages and Fan-Out WLPS, delves into the WLP market which includes the Fan-out WLP. The Fan-out WLP expands the number of I/O to beyond the perimeter of the die to form an array pattern on an overmold placed around the die during assembly. This increases the potential I/O count from what can otherwise be achieved on a WLP solution, while taking advantage of wafer level efficiencies.

Appendix A, Website Address Guide: This appendix contains the Internet addresses of the companies presented in this report, plus the OSAT companies that serve the IC packaging industry.

Appendix B, Glossary: This appendix contains a general glossary of terms used in the IC packaging industry.

1.4 Methodology

Information was obtained from both primary and secondary sources to complete this report. Information was gathered by telephone, e-mail, at trade shows, from speakers at seminars, conferences, luncheons and dinners, and by visiting companies in the industry. Secondary sources of information included company literature, trade magazines, seminar proceedings, and the Internet, and often led to further primary contact.

Hundreds of individuals were contacted for information for this report. They included key people within all of the major semiconductor fabrication companies and IC package foundries around the globe. Information was obtained using standard surveys and is printed only in the aggregate. The survey questions were designed to

determine the size of the market and likely growth patterns and to elicit responses about issues and developments in this particular area of the packaging industry. Discussions with those in the industry also played a key part in gathering information for this report.

A wide assortment of companies providing products and services for this market was contacted as well. Information was gathered in person when possible; company literature and white papers from seminars and proceedings were also heavily utilized.